



<https://g2-zero.com/jobs>

Job Description: **Clean Room Engineer**

g2-Zero, a spin-off of the Institute of Micro and Nanotechnology of the National Spanish Research Council (IMN-CSIC) and the Technical University of Madrid (UPM), has a position for a clean room engineer **to be filled before October 15th, 2022**.

He or she will join an international team of junior and senior research engineers and physicists developing Plug&Play single-photon sources for the emergent quantum technologies market.

The main tasks comprise the development of manufacturing processes for scaling up the fabrication of these devices.

Main Tasks and Responsibilities:

- Fabrication of III-V semiconductor devices using UVL, EBL, PE-CVD, ICP-RIE etc...
- Development of III-V microplanar fabrication recipes and processes.
- Design of mask layouts using K-Layout

Minimum Qualifications:

- Chemistry, Physics or Engineering Master Degree or equivalent.
- Candidates with a Bachelor degree might be considered depending on actual working experience.

Required knowledge and professional experience:

- Proven experience (3-4 years) with the manufacturing of III-V or Silicon based semiconductor/photonic devices using UV/EBL lithography, dry/wet etching, and metal/dielectric evaporation techniques.
- Proficiency in mask layout design using K-Layout scripting language.

Other skills:

- Experience in Semiconductor Packaging and Outsourced Semiconductor Assembly and Test will be positively considered

Job Conditions:

- Permanent contract
- Annual Gross Salary: 28,000 € - 32,000 € per year; commensurate with skills and experience.
- Health and Social benefits according to Spanish law

Application process:

- Send your updated CV and motivation letter to contact@g2-zero.com before **October 2nd, 2022** or subscribe to the offer posted in [LinkedIn](#).
- More information at <https://g2-zero.com/jobs>